

Doctor Number (Operator)

mv-99-00

Application Number

09/699,772

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"Sehat Sutardi, a

Filing Date

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U. S. PATENT DOCUMENTS

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AK	58501631215/98	Drost et al.	331	115	3/31/97
AK	59595049/28/99	Wang	331	117 FE	3/10/98
AK	57149112/3/98	Gilbert	331	57	2/6/96
AK	59492959/7/99	Schmidt	331	117 R	6/28/94
AK	600870112/28/99	Gilbert	331	57	3/2/98
AK	547534512/12/95	Gabara	331	117 FE	12/29/94

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER

DATE _____

COUNTRY

CLASS

SUBCLASS

Translation

YES

NC

OTHER DOCUMENTS (Including Author, Title, Date, Portion or Pages, Etc.)

"A 1.8 Ghz CMOS Voltage-Controlled Oscillator," B. Razavi, Digest of Technical Papers, 43rd ISSCC, 1997, pp. 388-389, and Fig. 4.
"Design Issues In CMOS Differential LC Oscillators," Hajimiri, A. et al., IEEE Journal of Solid-State Circuits, pp. 717-724, May 1999, Vol. 34 Issue No. 5.

EXAMINEE

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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U. S. PATENT DOCUMENTS

Doc# 1 Number (Operator)

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Profession Name

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

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OTHER DOCUMENTS (Including Author, Title, Date, Periodicals, etc.)

"10MHz CMOS OTA-C Voltage-Controlled Quadrature Oscillator,"
Linares-Barranco, et al., IEEE Electronics Letter, June 1989,
pp. 765-767, Vol. 25, Issue No. 12.

A Phase-Locked Loop with Phase-Locked Networks for RF Integrated Circuits

pp. 765-767, Vol. 25, Issue No. 12

pp. 765-767, Vol. 25, Issue No. 12.
"RC Sequence Asymmetric Polyphase Networks for RF Integrated
Transactions On Circuits and Systems, Part I: Analysis and Design"

"RC Sequence Asymmetric Polyphase Network"

Transceivers, Galal et al., Transactions on
Systems-11: Analog and Digital Signal Processing, Jan, 2000, Vol. 47,
Issue No.1, pp. 18-27.

Issue No. 1, pp. 18-27.

DATE CONSIDERED

4/14/02

EXHIBIT

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